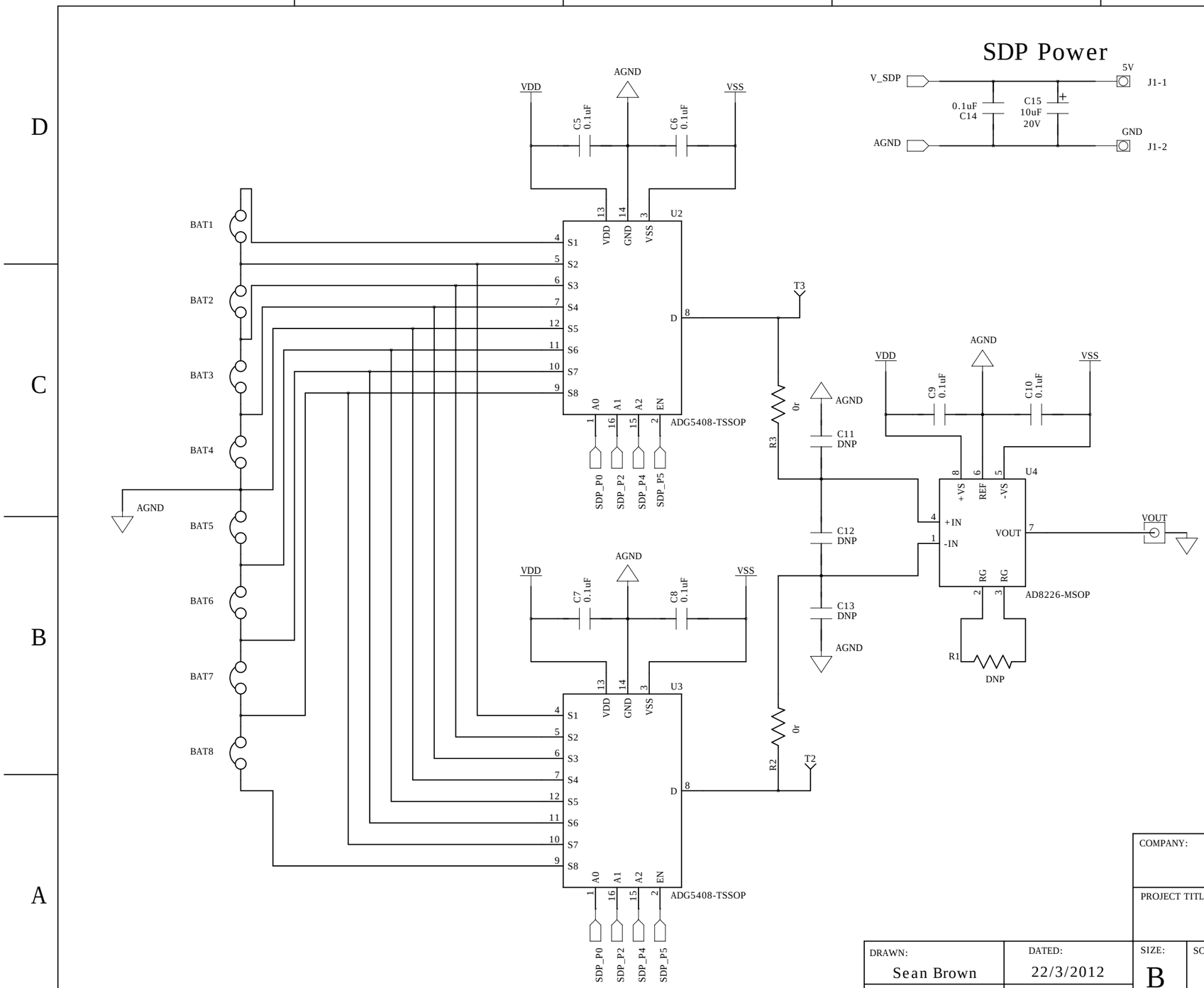
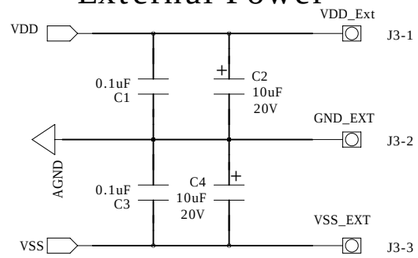
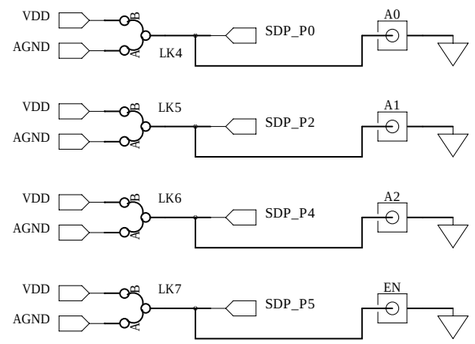


SDP Power

External Power



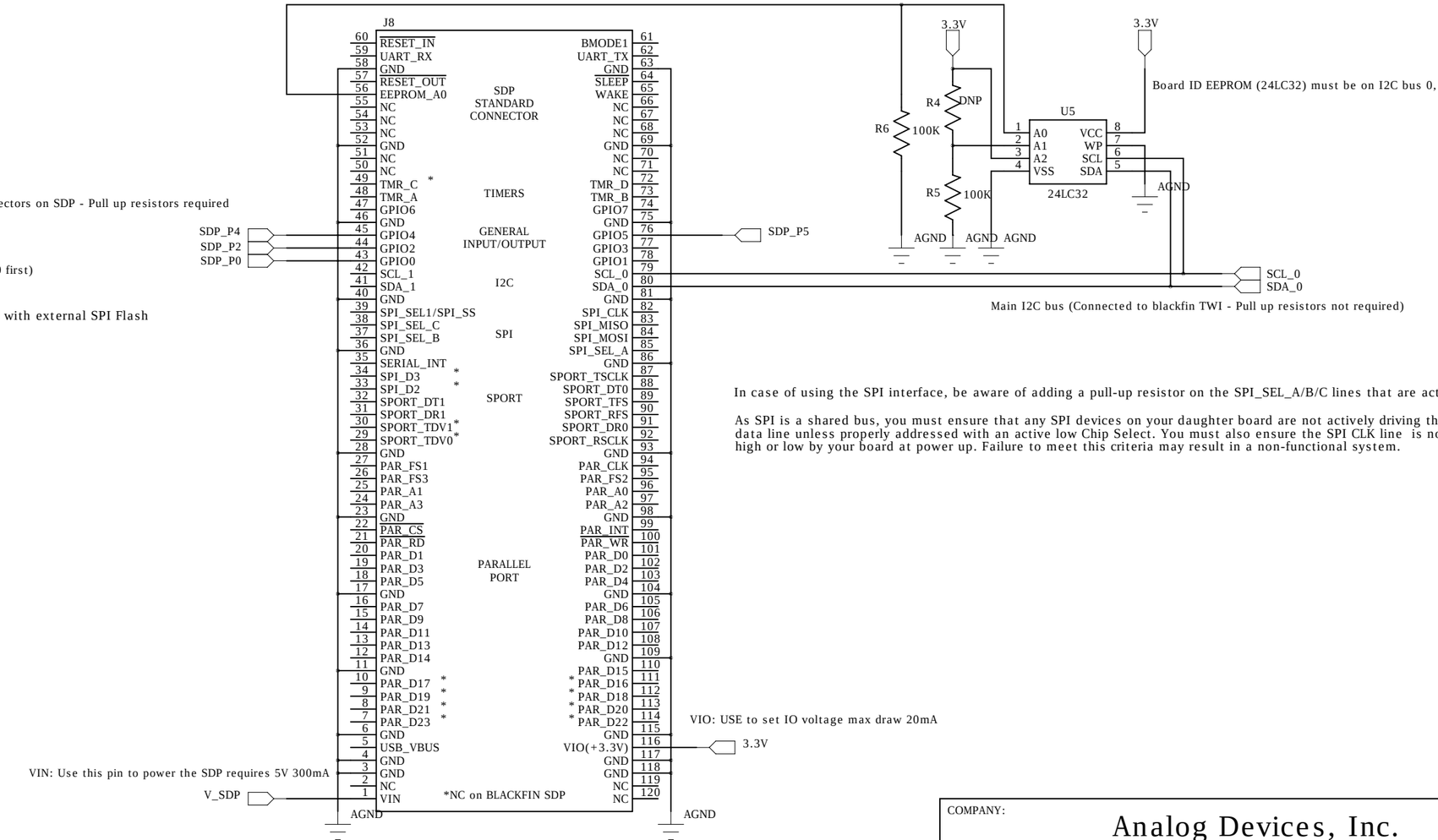
Manual Switch Select



COMPANY: Analog Devices, Inc.			
PROJECT TITLE: Battery Monitoring CFTL			
DRAWN: Sean Brown	DATED: 22/3/2012	SIZE: B	SCHEMATIC TITLE: SWITCH_AMP_POWER — REV: A
RELEASED: TBD	DATED: TBD	SCALE: 1:1	SHEET: 1 OF 2

The SDP Connector implements the EI3 connector specification standard. This is a standard for use across ADI and cannot be modified

BMODE1: Pull up with a 10K resistor to set SDP to boot from a SPI FLASH on the daughter board



I2C bus 1 is common across both connectors on SDP - Pull up resistors required

(connected to blackfin GPIO - use I2C_0 first)

SPI_SEL1/SPI_SS must be only used with external SPI Flash

Board ID EEPROM (24LC32) must be on I2C bus 0,

Main I2C bus (Connected to blackfin TWI - Pull up resistors not required)

In case of using the SPI interface, be aware of adding a pull-up resistor on the SPI_SEL_A/B/C lines that are active low enabled

As SPI is a shared bus, you must ensure that any SPI devices on your daughter board are not actively driving the MISO data line unless properly addressed with an active low Chip Select. You must also ensure the SPI CLK line is not held high or low by your board at power up. Failure to meet this criteria may result in a non-functional system.

COMPANY: Analog Devices, Inc.			
PROJECT TITLE: Battery Monitoring CFTL			
DRAWN: Sean Brown	DATED: 22/3/2012	SIZE: B	SCHEMATIC TITLE: NEW_SDP / REV: A
RELEASED: TBD	DATED: TBD	SCALE: 1:1	SHEET: 2 OF 2